

# NOT RECOMMENDED FOR NEW DESIGN CONTACT US



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
60V	$2\Omega$ @ V <sub>GS</sub> = 5.0V	350mA
60 V	2.5Ω @ V <sub>GS</sub> = 2.5V	330IIIA

#### **Description**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

#### **Applications**

- Motor controls
- · Power management functions

#### **Features**

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMN61D9UDWQ</u>)

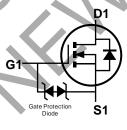
#### **Mechanical Data**

- Package: SOT363
- Package Material: Molded Plastic. "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

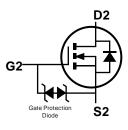




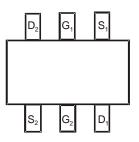
Top View



Q1 N-Channel



Q2 N-Channel



Equivalent Circuit

Top View Pin out

### Ordering Information (Note 4)

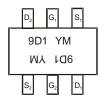
Part Number	Packago	Pack	king
Fait Number	Package	Qty.	Carrier
DMN61D9UDW-7	SOT363	3000	Tape & Reel
DMN61D9UDW-13	SOT363	10000	Tape & Reel

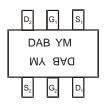
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



#### **Marking Information**





9D1 or DAB = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: J = 2022) M = Month (ex: 9 = September)

#### Date Code Key

Year	2015		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	С		J	K	L	М	N	0	Р	R	S	T
Month	lan	Feh	Mar	Anr	May	lun	hul	Aug	San	Oct	Nov	Dec
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

## **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic				Symbol	Value	Unit
Drain-Source Voltage				VDSS	60	V
Gate-Source Voltage			1	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 5V Steady State		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		lo	350 290	mA
Maximum Continuous Body Diode Forward Current (Note 6)			,	Is	0.4	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6) (Note 6			I <sub>DM</sub>	1.2	Α

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	397	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	410	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	306	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

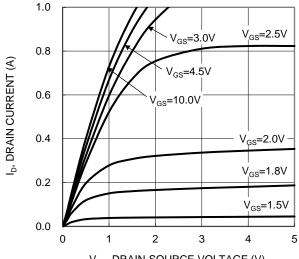


## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	_		V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.0	V	$V_{DS} = 10V, I_{D} = 250\mu A$
			1.2	2.0		$V_{GS} = 5.0V, I_{D} = 0.05A$
Static Drain-Source On-Resistance	RDS(ON)	_	1.6	2.5	Ω	$V_{GS} = 2.5V, I_{D} = 0.05A$
			2.5	3.5		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y <sub>fs</sub>	200	_	_	mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.4	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS (Note 8)			•			
Input Capacitance	Ciss	_	28.5	_	pF	201/1/
Output Capacitance	Coss	_	3.9	.—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.5	1	pF	T = 1.0WH2
Gate Resistance	Rg	_	65	-	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.4		nC	101/
Gate-Source Charge	Qgs		0.1		nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q <sub>gd</sub>		0.1		nC	I <sub>D</sub> = 250mA
Turn-On Delay Time	t <sub>D</sub> (ON)	1-1	2.1	_	ns	
Turn-On Rise Time	tr	1	1.8		ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	tD(OFF)	+	14.4	-	ns	$R_G = 25\Omega$ , $I_D = 200mA$
Turn-Off Fall Time	tF	7	8.4		ns	

7. Short duration pulse test used to minimize self-heating effect.8. Guaranteed by design. Not subject to product testing. Notes:





V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 1. Typical Output Characteristic

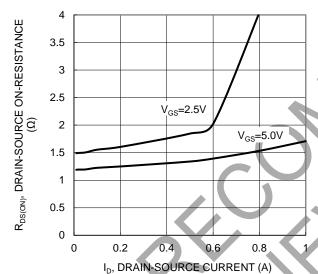


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

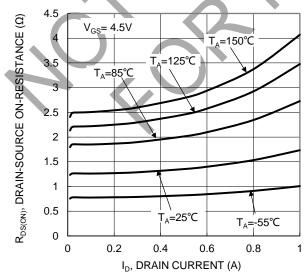


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

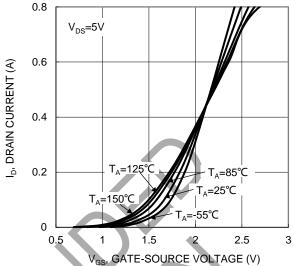


Figure 2. Typical Transfer Characteristic

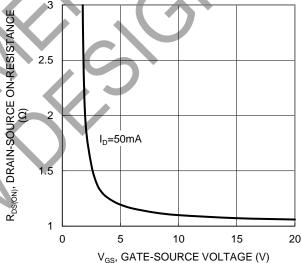


Figure 4. Typical Transfer Characteristic

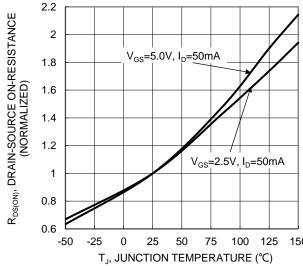


Figure 6. On-Resistance Variation with Junction Temperature





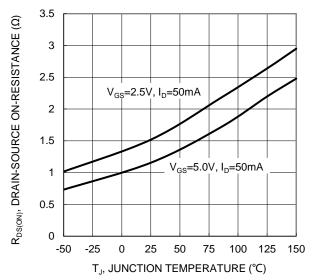
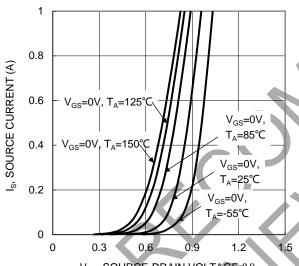
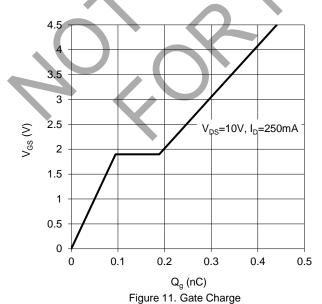


Figure 7. On-Resistance Variation with Junction Temperature

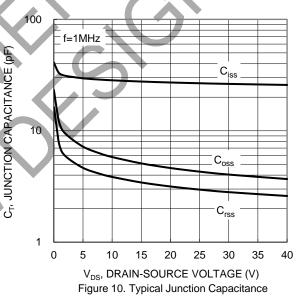


V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



1.2  $V_{GS(TH)}, \, \mathsf{GATE} \, \mathsf{THRESHOLD} \, \mathsf{VOLTAGE} \, (\mathsf{V})$ 1 I<sub>D</sub>=1mA 8.0  $I_{D} = 250 \mu A$ 0.6 0.4 75 125 -50 -25 50 100 150 JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



10 R<sub>DS(ON)</sub> Limited 1 ID, DRAIN CURRENT (A) 0.1 T<sub>J(MAX)</sub>=150℃ 0.01 T<sub>C</sub>=25℃ Single Pulse DUT on 1\*MRP board V<sub>GS</sub>=10V 0.001 10 100 0.1 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

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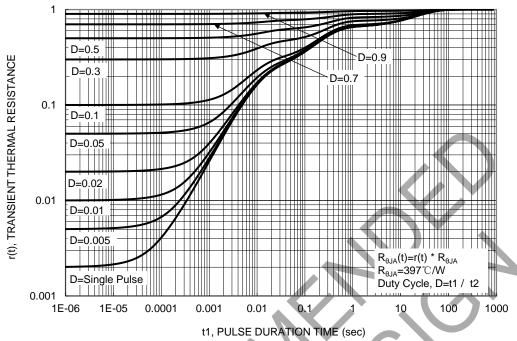
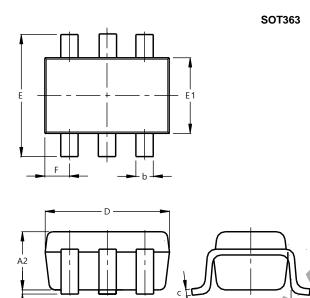


Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

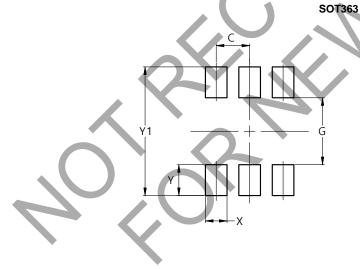
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT363							
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.10	0.30	0.25					
С	0.10	0.22	0.11					
D	1.80	2.20	2.15					
E	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	9	.650 B	SC					
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	Dimen	sions	in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value
Dimensions	(in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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